

**VERSION WITH MARKINGS TO SHOW CHANGES MADE<sup>1</sup>**

**In the Claims:**

Claims 48-53 have been amended as follows:

48. (Amended) An energy-conserving microprocessor or CPU system comprising:
- (a) keep-alive circuitry operable for performing auxiliary information processing when receiving keep-alive power; and
  - (b) main circuitry operable for performing main information processing only when receiving main power.
49. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is adapted to be also operable when said main power is present.
50. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is provided for controlling an activity of associated device means when said main power is absent.
51. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is provided for performing a keep-alive task when said main power is absent, said keep-alive task including to actuate said main circuitry when needed.
52. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said keep-alive circuitry is adapted to establish circuit communication with an interfacing means provided for transmitting a signal issued from an external [manual-operable] means so as to request said keep-alive circuitry to perform a requested activity selectively when said keep-alive power or said main power is present.
53. (Amended) The energy-conserving microprocessor or CPU system of claim 48, wherein said main circuitry is adapted to establish circuit communication with an interfacing means provided for transmitting a signal issued from an external [manual-operable] means so as to request said main circuitry to perform a requested activity when said main power is present.

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<sup>1</sup> Brackets "[ ]" indicate deletions and underlining "  " indicates insertions.